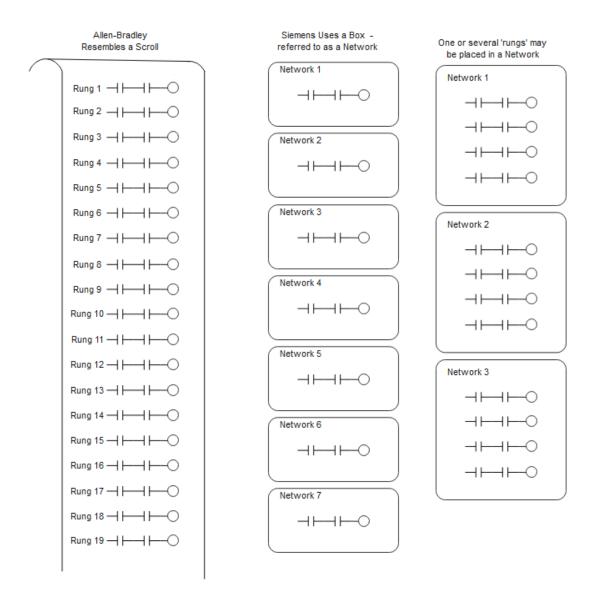
# Chapter 4 Programming the Application

This chapter deals with the steps to creating a working program from both the Siemens and A-B platform. In the last chapter, we dealt with linking the computer to the PLC and establishing the project or file for the PLC to begin programming. This chapter deals with the creation of programs for the PLC.

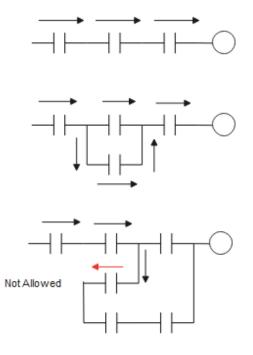
#### **Background Program**

Allen-Bradley and Siemens go about storing programs in a main file that runs continuously in the background of the processor. Allen-Bradley refers to this program as Main while Siemens refers to it as OB1 (Object Block 1). The method of storing logic looks slightly different for the two. At the left is Allen-Bradley's program format. It resembles a scroll. At right is the Siemens approach. Siemens allows the programmer to use one line or rung of logic per Network or place multiple lines of logic in the same network. An advantage of placing more than one line in the same network is that the logic is not as spread out and can be more easily read.



Ch. 4 Programming the Application

The following shows the path logic is solved by both manufacturers' PLC. The logic solves left to right, up to down and down to up. Never does the logic flow right to left. There may be a circuit that, if copied as is, would require right to left logic but this logic must be modified to not allow the flow right to left.



## Discussion of entry of the logic for both processors is given.

## First with Siemens:

## **Organizing the Program in S7**

The S7 PLC uses "program containers" called Organization Blocks to separate programs into areas of execution.

OBs are numbered and divided into different classes based on function. The quantity of OBs of a particular class vary by CPU model. OBs are added by the application programmer as desired. OB1 contains the main application program for the PLC. Other OBs can be thought of as interrupt handlers or subroutines (function blocks).

## What Are Organization Blocks?

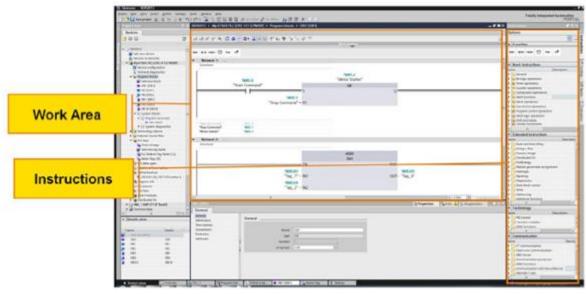
Organization Blocks (**OBs**) are the interface between the operating system of the CPU and the user program. OBs are used to execute specific program sections:

- At the startup of the CPU
- In a cyclic or clocked execution
- Whenever errors occur
- Whenever hardware interrupts occur

During a warm restart, the first step carried out by the operating system is to delete the nonretentive bit memories, timers, counters, interrupt stack, and block stack; to reset all stored hardware interrupts and diagnostic interrupts; to write the status of the outputs to the PIQ table (Process Image of Outputs); and to start the scan cycle monitoring time. In the second step, the operating system writes the values from the process-image output table into the output modules. In the third step, it then reads the status of the inputs and writes them to the PII Table (Process Image of Inputs). In the fourth step, it then executes the user program with the respective instruction. The operating system starts the cyclic operation again and continually monitors for interrupts.

## Startup:

A startup program is carried out before the cyclic program execution after a power recovery or a change of operating mode (through the CPU's mode selector or by the PG). OB 100 is available in every PLC model, whereas 101 and 102 are only available in the S7-400. In these blocks you can, for example, preset the communications connections or execute initialization routines.



## The Program Editor

Fig. 4-1 View of Base Page – Program Editor

The Editor has the following components:

## Instructions

This area shows all instructions available for use in the program. To implement an instruction, select the "rung" (network) you wish to add the instruction to, then "drag and drop" the instruction onto the "rung".

## Work Area

The code section contains the program itself, divided into separate networks (rungs) if required.

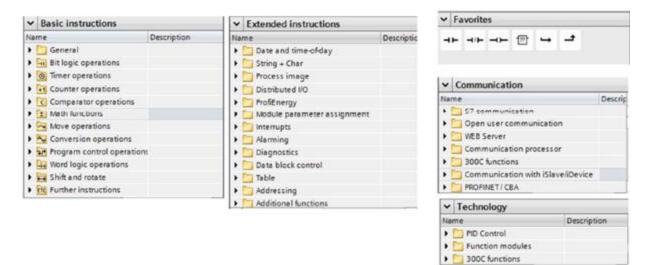


Fig. 4-2 Cross-Section of Instructions

Programming instructions are separated into groups based on functionality.

**Basic Instructions** – these are the "standard instructions" that can be used with all standard data types. Instructions include Bit Logic Operations, Math functions, Move Operations, and so on.

**Extended Instructions** – these are instructions that can either be used on complex data types, or that perform specific functions not associated with "traditional" program control functions.

**Favorites** – you can add frequently used instructions to the Favorites group. Instructions in the Favorites group appear in the upper area of the Program Editor.

**Communications** – these are instructions used to program communications tasks, such as peerto- peer PLC communications, open communications over supported networks, and so on.

**Technology** – these instructions are associated with technology functions supported by the PLC in question. Examples include PID Control.

## **Addressing Program Elements**

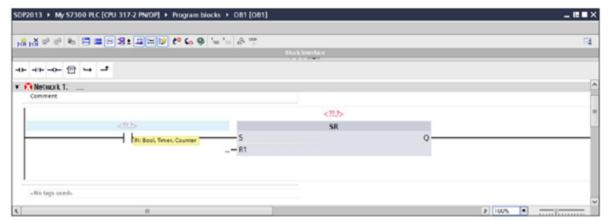


Fig. 4-3 Addressing a Function

As instructions are added, any address the user MUST fill in are shown in a red italic font. The program editor prompts you for the general data format of the instruction as follows:

- ??.? a bit address
- ??? an address other than a bit (e.g., byte, word, etc.)

Addresses that can be defined but are not required to be addressed are shown with a black font as an ellipsis (...).

Allowing the mouse pointer to "hover" over an address field will reveal the required data type(s) for the instructions operand. Blocks can be saved without addresses filled in, but the program will not compile until all required addresses are defined.

## **Programming with Tag Names**

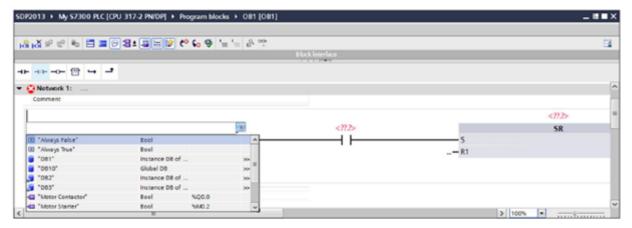


Fig. 4-4 Tag Selection from Menu

You can select configured tags using a pull-down accessible from the instruction. Double clicking will bring an icon into view that can be clicked on to open a list of all tags in the program that have the required data type. You can also start typing and the tags that appear will be filtered based on the text entered to that point. Select the desired tag from the list to assign the tag to the instruction. Autocomplete must be turned on to use this feature.

Statement Element	Description				
<tag></tag>	Tag that you access. The tag must be of the "Bit string" data type. In the case of activated IEC check, the access to tags of the "Integer" data type is also possible.				
X B W D	ID for the access width "Bit (1Bit)" ID for the access width "Byte (8 Bit)" ID for the access width "Word (16 Bit)" ID for access width "DWord (32-bit)"				
<bit number=""></bit>	Bit number within <tag> that is accessed. Number 0 accesses the least significant BIT</tag>				
<byte number=""></byte>	Byte number within <tag> that is accessed. The number 0 accesses the least significant BYTE.</tag>				
<word number=""></word>	Word number within <tag> that is accessed. The number 0 accesses the least significant WORD.</tag>				
<dword number=""></dword>	DWord number within <tag> that is accessed. The number 0 accesses the least significant DWORD.</tag>				

Fig. 4-5 Table of Memory Designators for Tags

In many cases, it is necessary to access portions of contiguous data that is "embedded" in a larger tag element. An example would be evaluating or changing a bit address within a word of data. To access a smaller segment of data by a Bit, Byte, Word or Double word, the syntax is as follows:

BIT	<tag>.x<bit number="">, e.g., "Status Long Word".X4</bit></tag>
BYTE	<tag>.B<byte number="">, e.g., "Status Long Word".B2</byte></tag>
WORD	<tag>.W<word number="">, e.g., "Status Long Word".W0</word></tag>
DOUBLE WORD	<tag>.D<double number="" word="">, e.g., "Status Long Word".D1</double></tag>



## **Changing an Operand**



Often it is required to change an operand to a different one. To accomplish this, double click on the instruction, then click on the pull down to select the new operand.

The same feature can be used for many of the math instructions where the data type of the instruction can be changed using the same method.

A first contact is chosen and added to the first network, Network 1:

Project21 → PLC_1 [CPU 1214C DC/DC/DC] → Program blocks → I	Main [OB1] 🛛 🗖 🖬 🗙
📸 🖓 🦈 🔹 🚍 🚍 💬 署 ± 🖀 🚍 🐲 🚱 '= '	
Block interface	
	Fig. 4-6 Choosing LAD as a Programming Language
<ul> <li>Block title: "Main Program Sweep (Cycle)"</li> </ul>	
Comment	
▼ 😧 Network 1:	
Comment	=
?.?	×
Fig	. 4-7 Network 1

Notice the <??.?> above the contact. This signals that an address must be selected for this contact, much as a name was required for each contact in a ladder diagram. Move to the tree area PLC tags and choose Show all tags. This area is blank to begin and must be added before the contact above is complete. Bit, byte and word length tags may be created here. Some care must be given to the addressing since tags can be programmed over other tags, a problem that will cause errors later in debugging.

Project tree	•	Project	21 → PLC_1	[CPU 1214C DC/DC/	'DC] → PLC tags		_₽≣	×
Devices				🕣 Ta	ags 🗉 User con	stants 🖉 🗶 Sys	tem constants	
B 0 0 E	Ł	🥩 🖻	) 🗗 🗗 🕯	x			2	
		PLC	tags					
▼ 1 PLC_1 [CPU 1214C DC/D	^	-	Name		Tag table	Data type	Address	Re
Device configuration		1	<add new=""></add>					6
🖳 Online & diagnostics								
🕶 🚘 Program blocks								
📑 Add new block								
💶 Main [OB1]								
🕨 🚂 Technology objects								
🕨 🔚 External source files								
🕶 🌄 PLC tags								
📚 Show all tags								
📑 Add new tag table						E' 40 A	11. 7	
🍯 Default tag table [						F1g. 4-8 A	dding Tags	
PLC data types								

To start, tags will be given generic names such as "a1", "a2", etc. This is not a good practice but Ch. 4 Programming the Application 7 will be done to start the process of naming variables. Tags should be given meaningful names that give the user an idea as to the meaning behind the contact or other instruction. Below, when the first tag is entered, an address appears of %10.0. If your tag is to be addressed to the first input %10.0 then all is well. Usually, this variable needs to be changed. Here it is changed to an M identifier. M bits and bytes are used for internal storage, not for inputs or outputs from the PLC. The first bit of the M table is M0.0. Since this table is addressed in bytes, the succeeding bits are M0.1, M0.2, M0.3, M0.4, M0.5, M0.6, M0.7, M1.0, etc.

Project tree 🛛		Project2	1 → PLC_1 [CPU 1	214C DC/DC/DC	]   PLC tags			_	∎≡×	Tasks	<b>.</b> D	
Devices				🕣 Tags	🗉 User co	nstants	🗶 Sys	tem const	tants	Options		
B C C E	•	∌ ₹	🖻 🛃 🛈 X									Tasks
		PLC	tags							✓ Find a	nd replace	<u>s</u>
▼ 🛅 PLC_1 [CPU 1214C DC/D	^		Name	Та	g table	Data typ	e	Address	Re			
Device configuration		1 🕣	a1	De	efault tag table	<ul> <li>Bool</li> </ul>		%I0.0	-	Find:		
😵 Online & diagnostics		2	<add new=""></add>									
🕶 🔙 Program blocks									Operand	identifier:	М	
💣 Add new block									Ope	rand type:		
📲 Main [OB1]										Address:	0	
🕨 🙀 Technology objects	_									t number:	0	
External source files	=									thumber.	0	
🕶 🚂 PLC tags												
li tags												
💕 Add new tag table										Use re	egular expression	ns
💥 Default tag table [	-									O Whole	e document	
PLC data types										_		
Watch and force tables										From	current position	
Program info		*							>	🕘 Selec	tion	

Fig. 4-9 Using M for Internal Data Storage

Thus, the first address is entered as M0.0.

Projec	ct2	1 → PLC_1 [CPU 1214C D		DC] )	<ul> <li>PLC tags</li> </ul>						×
		-	💷 Ta	gs	🗉 User c	ons	tants 🐙	) Sys	tem consta	ants	
<u> </u>	ŝģ										
PL	C t	ags									
		Name		Tag ta	ble		Data type		Address		Re
1	-	a1		Defau	ılt tag table	•	Bool		%M0.0	-	0
2		<add new=""></add>									E

Fig. 4-10 We Entered M0.0

And we proceed back to the ladder diagram for Network 1 and add the tag to the contact:

😮 Network 1: 📖			
Comment			
I	Bool	%M0.0	,
	6001	/01/10.0	
-			

Fig. 4-11 Completed Tag

The final result resembles the following:

•	Network 1	:
	Comment	
-	<b>%MO.O</b> "a1" ┃	

Fig. 4-12 Continuing with Network 1

Next, we would like to add an input to the logic. The input is tied to the input point 10.0. In the PLC tag table, we begin with the name "Input0". We proceed across with default tag table, Bool and then see an address of %M0.1 picked. This must be changed. The tag table will automatically roll to the next available address but we will be using an internal bit for an input but rather an "I" bit, 10.0.

Proje	Project21 → PLC_1 [CPU 1214C DC/DC/DC] → PLC tags _ ■ ■ ×										
	📹 Tags 🗉 User constants 🔀 System constants										
<b>#</b>	🚅 🔮 🖶 🕅 🖬 🔁										
F	PLC tags										
	-	Name	Tag table	Data type	Address Re						
1		a1	Default tag table	Bool	%M0.0						
2		Input0	Default tag table 💌	Bool 🔳	%M0.1 🔽 🤅						
3		<add new=""></add>			[						

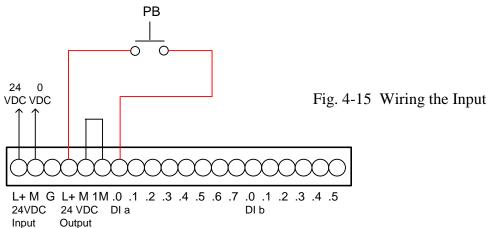
Fig. 4-13 Adding the Input

After changing the M address reference to an I address, and entering the correct bit offset, the table appears as follows:

Proje	Project21 → PLC_1 [CPU 1214C DC/DC/DC] → PLC tags _ ■ ■ ■ ×										
	Tags 🗉 User constants 🔀 System constants										
	🚽 🔮 🖶 🛍 🖂 🔤										
P	PLC tags										
	-	Name	Tag table	Data type	Address Re						
1		a1	Default tag table	Bool	%M0.0						
2	-00	Input0	Default tag table 💌	Bool 🔳	%10.0						
З		<add new=""></add>			[						

Fig. 4-14 Completing the Tag for the Input

Do not forget that a real device needs to be wired to an input for this input to perform its correct function. Usually the input wired is through a NO (normally open) contact. This may change from time to time but NO is usually chosen.



In the Program Block, the contact is added and the tag Input0 chosen.

🔻 😧 Network 1: 🛛			
Comment			
%M0.0 "a1"			
	💷 "a1"	Bool	%M0.0
	<ol> <li>Input0<sup>®</sup></li> </ol>	Bool	%10.0

Fig. 4-16 Adding the Tag for the Input

Our program now has a normally closed contact labeled "Input0" address 10.0 in series with a normally open contact labeled "a1" address M0.0. Next, we would like to add a parallel contact to the NO contact "a1". Start with an arrow from the left ladder.

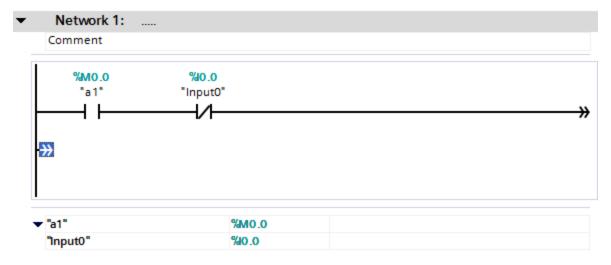


Fig. 4-17 Adding a Parallel Path

Add a tag to the tag table "a2". Note that we want this address as an M bit so the next available M bit is M0.1.

Project21 → PLC_1 [CPU 1214C DC/DC/DC] → PLC tags _ ■ ■ ×											
	🕣 Tags 🗉 User constants 🔀 System constants										
،											
PL	PLC tags										
		Name	Tag table	Data type	Address Re						
1	-	a1	Default tag table	Bool	%M0.0						
2	-	Input0	Default tag table	Bool	%10.0						
3	-	a2	Default tag table 💌	Bool	🗐 %M0.1 💌 [						
4		<add new=""></add>									

Fig. 4-18 Adding the Tag for the Parallel Path

We finish the contact by choosing the normally closed contact and adding the "a2" tag from the tag list:

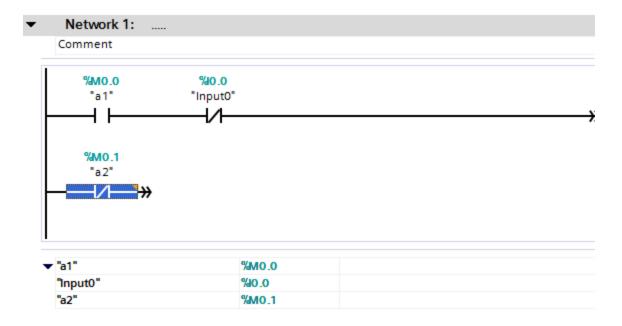


Fig. 4-19 Adding the Contact for the Parallel Path

The up arrow is chosen to tie the circuit right of "a2" to the circuit above. The circuit is now complete except for an output coil.

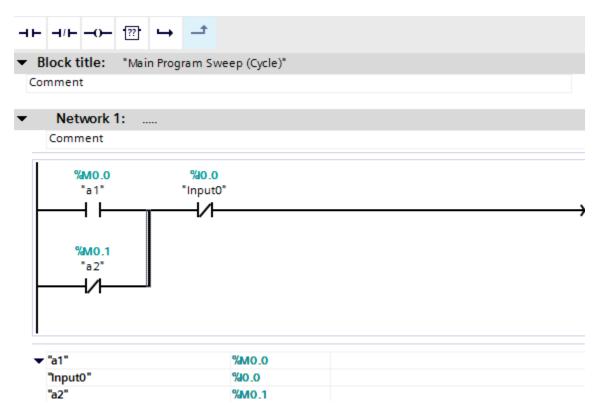


Fig. 4-20 Tying in the Parallel Path

The output coil is chosen from the instruction list and the tag is added.

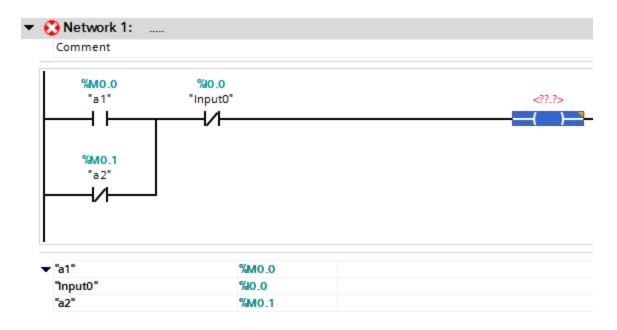


Fig. 4-21 Adding the Coil

The tag is chosen from the internal M bits again, this time M0.2.

Proje	Project21 → PLC_1 [CPU 1214C DC/DC/DC] → PLC tags _ ■ ■ ×								
	Tags ■ User constants     System constants								
PL	PLC tags								
		Name	Tag table	Data type	Address Re				
1	-	a1	Default tag table	Bool	%M0.0				
2	-	Input0	Default tag table	Bool	%10.0				
3	-	a2	Default tag table	Bool	%M0.1				
4	-00	b1	Default tag table 💌	Bool 🔳	%M0.2				
5		<add new=""></add>							

Fig. 4-22 Adding the Tag to Finish the Coil

At this time, our circuit is complete. The next rung or circuit is to be programmed in the next available location. The programmer has a choice of moving to the next network, Network2, or continuing in the present network. The program will solve the same either way. Usually, the programmer will continue in the same network for compactness on the screen. More can be seen at the same time when troubleshooting if more rungs are grouped into the same network.

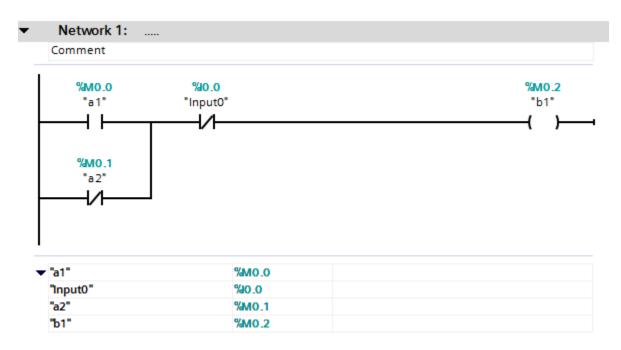


Fig. 4-23 Completed Circuit (Rung)

The following shows a second circuit input in the same network as the first. This circuit is not completed but illustrates the ability of the programmer to stack several ideas or circuits into one network.

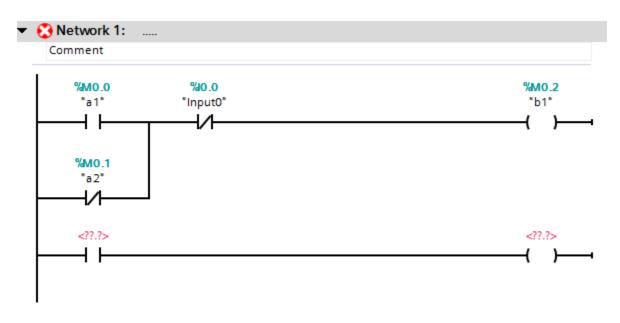


Fig. 4-24 Where to Add Another Circuit (Rung)

The following shows the same circuit but entered in a second network. Here the ideas are more spread out, usually a less attractive alternative but available as desired.

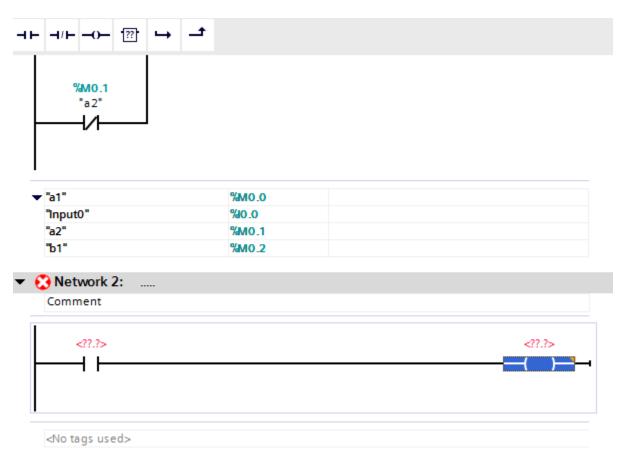
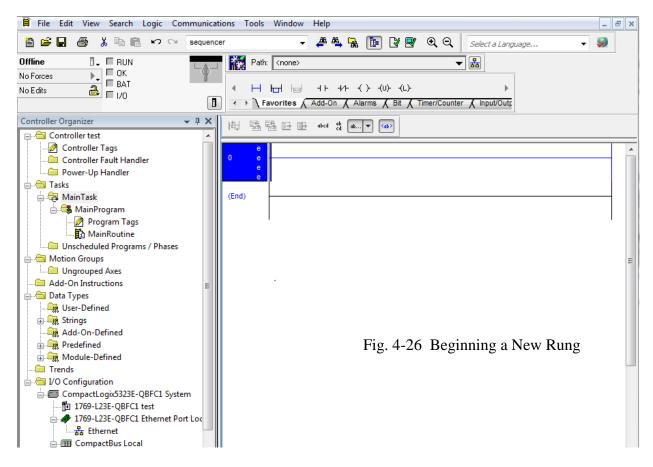


Fig. 4-25 Alternate Place Add Circuit (Rung)

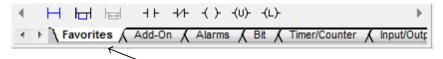
When defining a Boolean variable, the choice of local or global is to be given. The only time that a variable is to be defined as a Local variable is when the information is only to be passed to a later rung in the same scan but lost after this. The local variable does not return after the end of the scan. Care must be taken to not choose 'Local' because it is the default choice. It is not the preferred choice in most cases.

## Next, Allen-Bradley

Starting with the project tree, the first program to enter is MainRoutine under MainProgram. This program is equivalent to OB1 in that it is always on and scanning in the background. Execution occurs as often as possible when other programs are not pre-empting the cpu's time. This program is programmed in Ladder. Subroutines and other programs may be programmed in FBD and STL.



To choose a NO contact, either of the following tabs may be chosen.



Choose either the Favorites tab above or the Bit tab below to show a NO contact. NC contact and coil are found in both as well.

				H۲				ĸ				•
۰.	$\rightarrow 1$	Favorit	es (	Add-0	n (	Alarm	sλ	Bit Á	Time	r/Coun	ter (	Input/Out

Fig. 4-27 Alternate Tabs for NO Contact

Tags are given the same generic names as with the Siemens processor but care must be taken to be meaningful to the process being represented. Names generally are less than 30 characters in length and may have underscore (\_). The more well commented, the better in the long run.

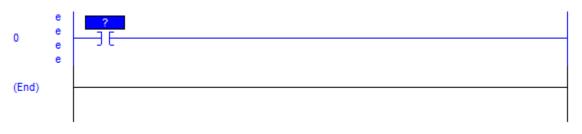


Fig. 4-28 Entering the NO Contact

The tag may be entered by right clicking the contact. The new tag will then be entered from the following screens:

0	e e e	?		New Tag	
(End)	e		ж Ра	Cu <u>t</u> Instruction <u>C</u> opy Instruction <u>P</u> aste	Ctrl+X Ctrl+C Ctrl+V
				Delete Instruction Add Ladder Element Edit Main Operand Description Save Instruction Defaults Clear Instruction Defaults	Del Alt+Ins Ctrl+D
				Toggle Bit <u>F</u> orce On F <u>o</u> rce Off R <u>e</u> move Force	Ctrl+T
					Ctrl+G F1

Fig. 4-29 Entering the Tag

The screen below will be entered with Name as a1. A description may be entered if desired. Since the contact was chosen, the Data Type is Bool. Other options are listed but usually left as is.

When the tag name is successfully entered, the contact and tag appear as one unit.

It is worth noting that the A-B tag database has no M offsets similar to the Siemens architecture. The variables' offset is hidden from the user. This is more like a computer language in which the value of a variable's address may not be known.

	Name:	2	OK	
	Description:		Cancel Help	
	Usage:	<normal></normal>		
	Туре:	Base   Connection		
	Alias For:			
	Data Type:	B00L		
	Scope:	🕞 MainProgram 👻		
	External Access:	Read/Write		
	Style:	Decimal 💌		
	🔲 Constant			
	🗌 Open Conf	iguration		
e 0 e e	a1			
(End)				

Fig. 4-30 Addressing a Tag using A-B

Tags may also be entered from the Program Tags option from the project tree. Here, they are entered in the Edit Tags mode (see tab at bottom of page). This mode must be properly set to enter tags or monitor tags. Use the Monitor Tags mode when online and changing variables to verify the program or enter data to try for a specific result. This tag will be discussed more in the troubleshooting section.

So	cope: 🕞 M	lainProgr	am 👻	Show: /	All Tags	-	<b>Y.</b> Enter Ne	me /	Silter	•
	Name		Alias For		Base Tag	Data Type	Descrip 🔺	Pr	operties	C
	a1					BOOL			<b>?</b> ≵↓ 🗉 🖌	
Þ									General	
								÷	Description	Ш
							E		Data	
									Produced C	
								Ľ	Consumed	
4		Togo								
٩.	Monito	n rags	A cult lag	js /	III.		P			

Fig. 4-31 Tag Entry from the Program Tags Option

Input and output tags are already defined and may be entered using their address. The addresses for these devices can be found under the controller tab Controller Tags. This table is set for the L23E. Other controllers with stacked cards will vary with the card type and number of each. For our processor, the following I/O list is standard.

🖃 🔄 Controller test				л
Controller Tags	b l	Name 📰 🛆	Value 🔶	Force Mask 🗧 🗧 Style
Controller Fault Handler		⊞-Local:1:C	{}	{}
Power-Up Handler		+-Local:1:I	{}	{}
Tasks		+-Local:2:C	{}	{}
and Tasks		⊞-Local:2:I	{}	{}
🖬 🕞 MainProgram		⊞-Local:2:0	{}	{}
Unscheduled Programs / Phases		⊞-Local:3:C	{}	{}
🚍 📇 Motion Groups		⊞-Local:3:I	{}	{}
Ungrouped Axes		⊞-Local:3:0	{}	{}
Add-On Instructions		⊞-Local:4:C	{}	{}
🚍 🚔 Data Types	Ξ	⊞-Local:4:I	{}	{}
🖳 🙀 User-Defined		⊞-Local:4:0	{}	{}
👜 🔙 Strings				
🔤 🔤 Add-On-Defined				
🕀 🛺 Predefined				
Module-Defined				

Fig. 4-32 I/O Tags Configured for the Processor

Expand the Input tab to find the specific input point to be used. For input point 0, Local:1:I.Data.0 is used.

cope: 🛐 test 👻			Lo. I
Name	Value 🗲	Force Mask 🗧 🗧	Style
±-Local:1:C	 {}	{}	
- Local:1:I	 {}	{}	
+-Local:1:I.Fault	 2#0000_00		Binary
E-Local:1:I.Data	2#0000_00		Binary
—Local:1:I.Data.0	 0		Decimal
—Local:1:I.Data.1	 0		Decimal
—Local:1:I.Data.2	0		Decimal
—Local:1:I.Data.3	0		Decimal
—Local:1:I.Data.4	0		Decimal
-Local:1:I.Data.5	0		Decimal
-Local:1:I.Data.6	0		Decimal
—Local:1:I.Data.7	0		Decimal
-Local:1:I.Data.8	0		Decimal
-Local:1:I.Data.9	0		Decimal
—Local:1:I.Data.10	0		Decimal
—Local:1:I.Data.11	0		Decimal
—Local:1:I.Data.12	0		Decimal
—Local:1:I.Data.13	0		Decimal
—Local:1:I.Data.14	0		Decimal
Local:1:I.Data.15	0		Decimal
+-Local:2:C	{}	{}	
±-Local:2:I	{}	{}	
±-Local:2:0	{}	{}	
+-Local:3:C	{}	{}	

Fig. 4-33 Expanding the I/O Table to get Actual Tag

This data address may be copied into the contact directly and used for the address.

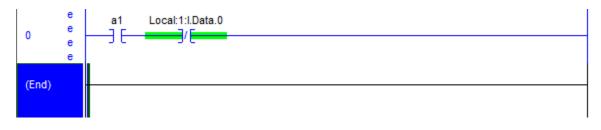
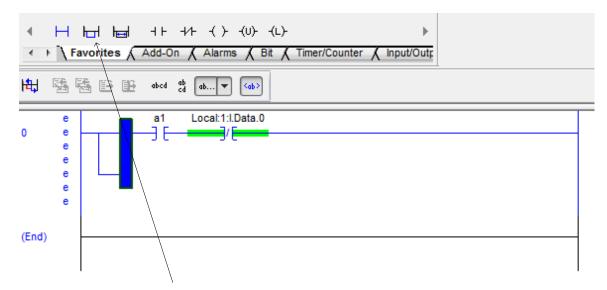


Fig. 4-34 Copy/Paste of I/O Tag into Rung

Adding a parallel branch involves the following:



From the Favorites, choose the loop (second choice) and place the loop before the contact to be branched around. Then drag the cursor around the contact. Then move the cursor just before the contact to be added.

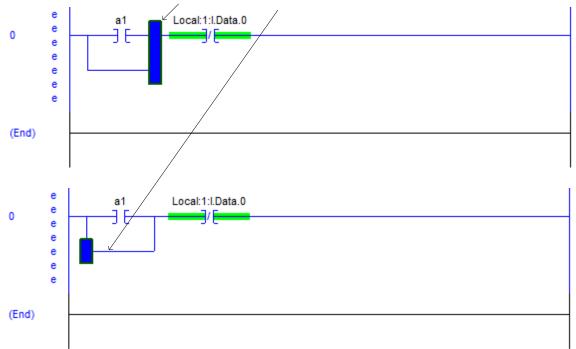


Fig. 4-35a Adding the Parallel Path

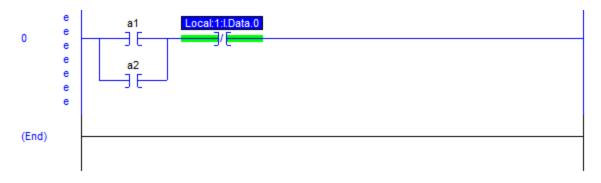
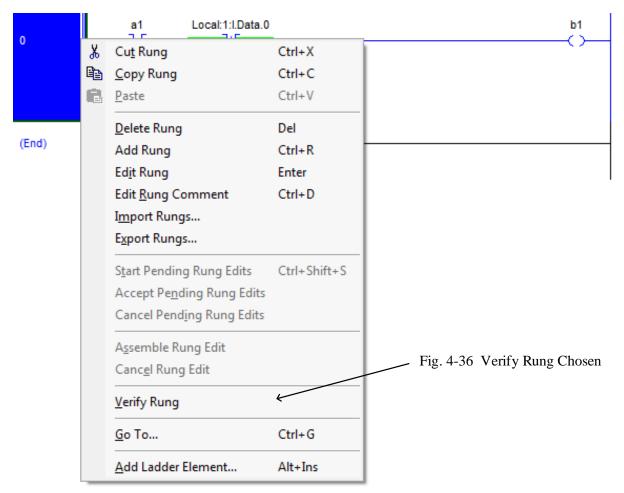




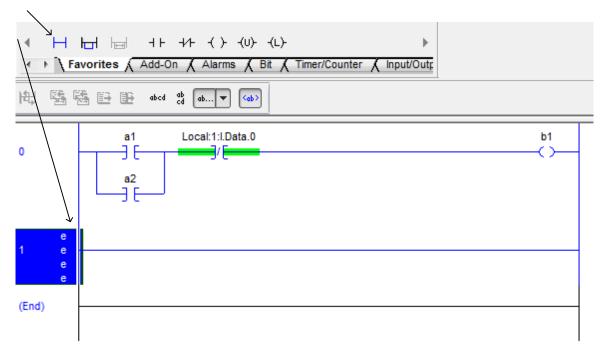


Fig. 4-35c Adding the Output

Once the rung has been completed, it is wise to verify the rung for completeness. Right click on the rung at left. The following will appear. Choose Verify Rung and the eee's should disappear.

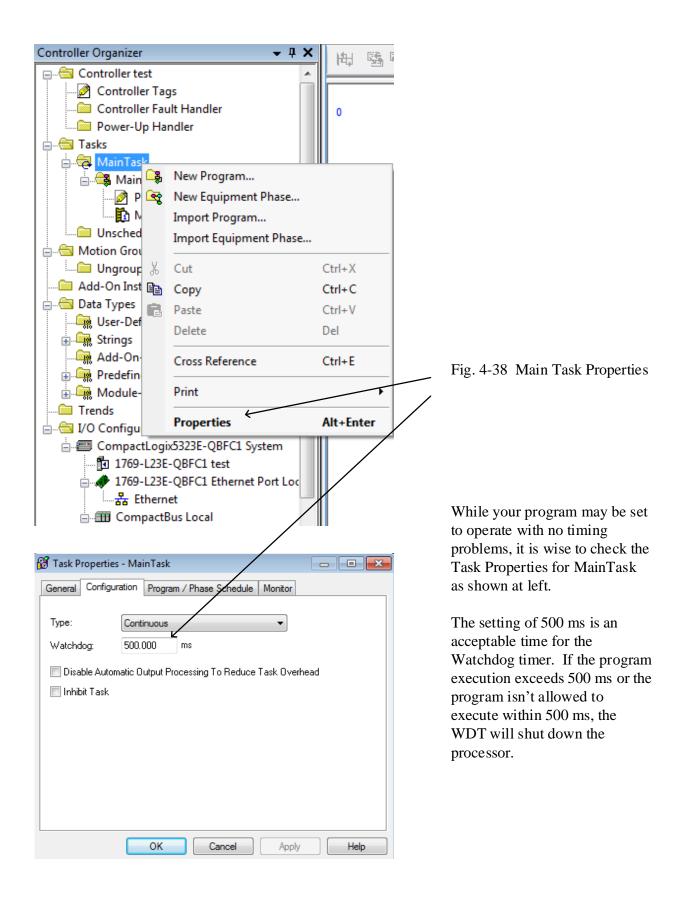


Ch. 4 Programming the Application



To start a second rung, simply click on the new rung button. The following will appear.

Fig. 4-37 Adding the next Rung

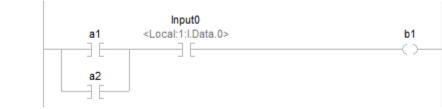


You may alias a tag to another name as shown in the example below. Here Input0 is aliased to Local:1:I.Data.0.

Se	cope: 🕞 M	lainProgr	am 👻 Show:	All Tags		▼ ¥. Enter Na	me Filter 🗸 👻
	Name	<b>18</b>	Alias For	Base Tag	Data Type	Descrip 🔺	Properties (9
	al				BOOL		📳 ĝ↓ 💷 🗲
	a2				BOOL		· General
	Ь1				BOOL		Description
×	Input0		Local:1:I.Data 👤		DINT		🕀, Data
<i>»</i>			T. Enter Name H	Ther 🖵 🤤	Show: All Tags		Produced Con
			Name	== Data T	ype Desc	ription	Consumed Con
			a1	BOOL	I		
			a2	BOOL			
			b1	BOOL			
			∎ +-Local:1:C		bedde		
			Local:1:1		bedde		
			Local:1				
			Local:1	:I.Data 🖵 INT			
				2 3 4 5	6 7		
			B H 8 Used:		14 15		*
			Contruin	lsed: N			
			Program				

Fig. 4-39 Aliasing of a Tag

Se	cope: 🕞 M	ainProgr	am 👻 Show: A	All Tags	▼ [×
	Name		Alias For	Base Tag	Data Type
	a1				BOOL
	a2				BOOL
	Ь1				BOOL
×	Input0		Local:1:I.Data.0 💌		BOOL
٦					



1

Fig. 4-40 How Aliasing Looks in the Program

Finally an A-B L16ER processor wired for inputs and outputs:

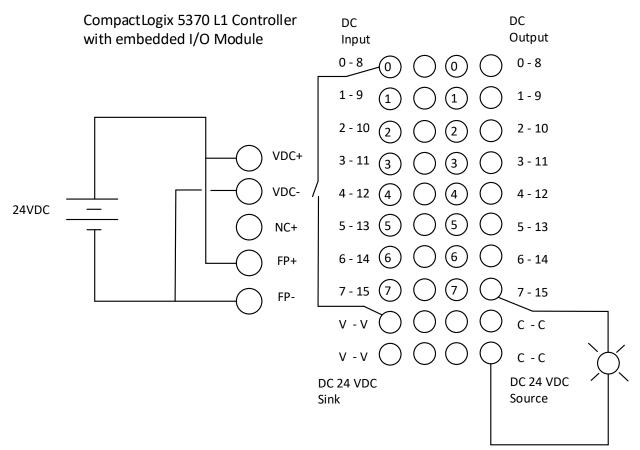


Fig. 4-41 (From Ch. 3)

## **Troubleshooting the Siemens Processor**

#### Online mode

In online mode, there is an online connection between your programming device / PC and one or more devices.

An online connection between the programming device/PC and the device is required, for example, for the following tasks:

- Testing user programs
- Displaying and changing the operating mode of the CPU
- Displaying and setting the date and time of day of the CPU
- Displaying module information
- Comparing blocks
- Hardware diagnostics

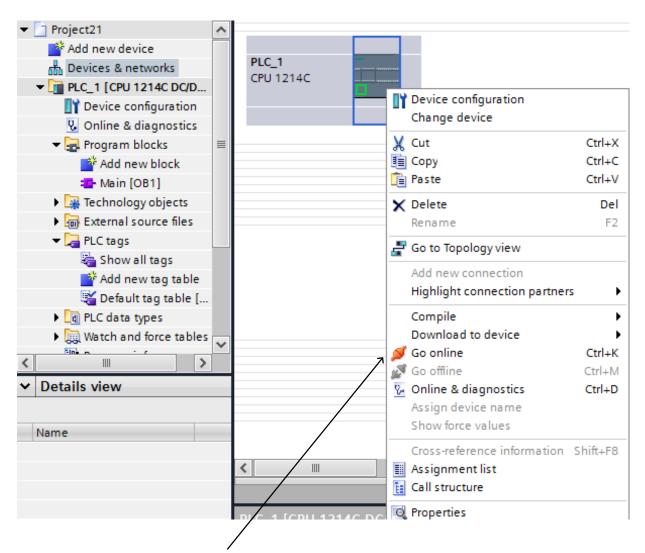
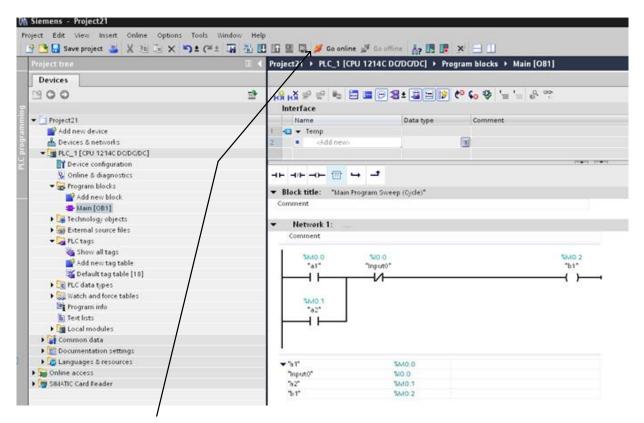
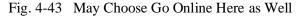


Fig. 4-42 Choosing to Go Online





tatus	!	Target	Message	Action
<b>+</b> []	0	▼ PLC_1	Ready for loading.	
	0	✓ Software	Download software to device	Consistent download
	0	<ul> <li>Overwrite onli</li> </ul>	Objects that exist online and will be overwritten.	
	0	Tags		<ul> <li>Image: A start of the start of</li></ul>
	0	Main [OB1]		<ul> <li>Overwrite</li> </ul>
	0	<ul> <li>Delete online</li> </ul>	Objects that only exist online and will be deleted.	
	0	Counter1 [		🖌 Delete
	0	IEC_Counte		🖌 Delete
				Refresh

Several changes appear when in the online mode. Among them are the following:

- 1. The title bar of the active window now has an orange background.
- 2. The title bars of inactive windows for the relevant station now have an orange line below them.
- 3. An orange, pulsing bar appears at the right-hand edge of the status bar. If the connection has been established but is functioning incorrectly, an icon for an interrupted connection is displayed instead of the bar. You will find more information on the error in "Diagnostics" in the Inspector window.
- 4. Operating mode symbols or diagnostics symbols for the stations connected online and their underlying objects are shown in the project tree. A comparison of the online and offline status is also made automatically. Differences between online and offline objects are also displayed in the form of symbols.
- 5. The "Diagnostics > Device information" area is brought to the foreground in the Inspector window.

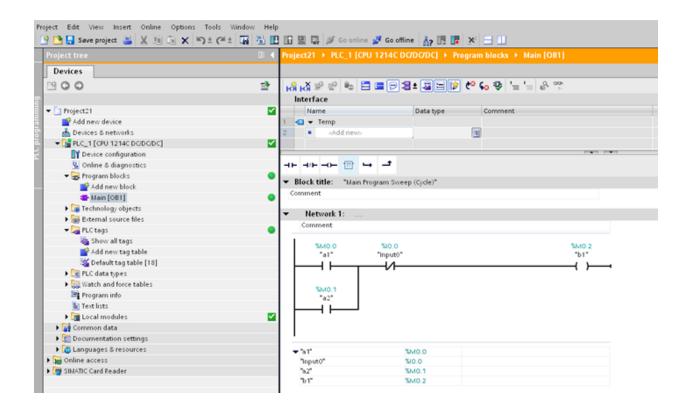


Fig. 4-45 Online Siemens Display

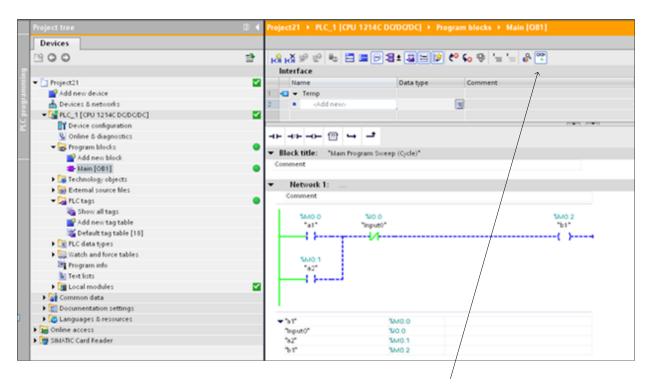


Fig. 4-46 Click on the Glasses to see Monitor Mode

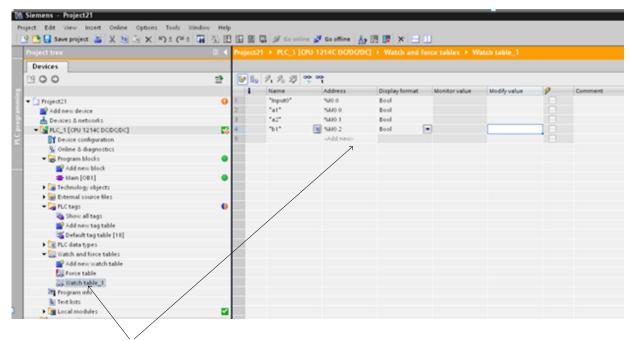


Fig. 4-47 Build a Watch Table to Monitor Variable Status

ogram blocks → Main [OB1]	_ ₽ ≣ X Testing ₽ 🗈 ►	
	Options	
C € ♥ '= '= ♥ ♥	I I I I I I I I I I I I I I I I I I I	Inst
	✓ CPU operator panel	ruct
Comment	PLC_1 [CPU 1214C DCIDODC]	Instructions
	RUN / STOP PUN	
		2
	MAINT MRES	Testing
		ting
	<u>^</u>	1
	✓ Call environment	Tasks 💽
	No trigger applied.	Tas
	Change	S
1M0.2		
"b1"		EF.
{ }{		🖯 Libraries
	-	es
	▼ Breakpoints	
Stop ×	응/± @ <sup>3</sup> ♥/ @> > > 11 도미 도미	
A	Test mode	
Are you sure you want to change the CPU to STOP mode?	Enable output in run	
	This device does not support breakpoints.	
OK. Cancel		
	V PLC register	
	BR CCO CC1	
	OV OS ORIo	
	STA PLO /FC	

Fig. 4-48 Starting and Stopping the Program

## **Troubleshooting the Allen-Bradley Processor**

and the second	000 test [1769-L23E-QBFC Wew Search Logic Communica	(1 19.11)* - [MainProgram - MainRoutine] Nons Tools Window Help	- 8 ×
1	. XBB PON	💌 🚑 强 🐚 🕼 🕅 🍳 🔍 Solect a Language	<b>N</b>
Rem Run	🚺 📕 Run Mode	ABLE Path: AB_ETHIP-1\131.183.18.223\Backplane\0"	
No Forces	Controller OK	Patr: AB_ETHIP-1\131.183.18.223\Backplane\0*	
No Edits	Battery OK	<ul> <li>         ・・・・・・・・・・・・・・・・・・・・・・・・・・・・・</li></ul>	
	- NO ON	Favorites Add-On A Alarms A Bit A Timer/Counter A I	

Access Who either from Communications or from the symbol here: `

Fig. 4-49a Downloading to the CompactLogix Processor

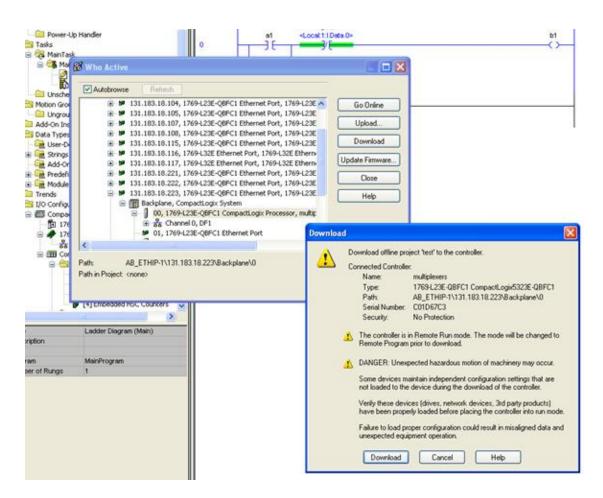
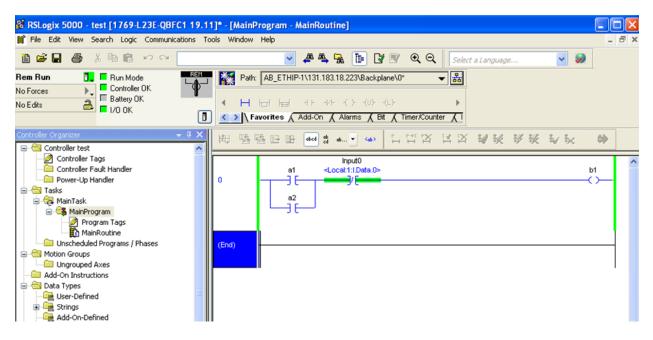


Fig. 4-49b Downloading to the CompactLogix Processor

Who Active	Refresh	- 0 2
● M 131 ● M 131 ● M 131	.183.18.104, 1769-L23E-QBFC1 Ethernet Port, 1769-L23E .183.18.105, 1769-L23E-QBFC1 Ethernet Port, 1769-L23E .183.18.107, 1769-L23E-QBFC1 Ethernet Port, 1769-L23E .183.18.108, 1769-L23E-QBFC1 Ethernet Port, 1769-L23E gix 5000	Go Online Upload
	Done downloading. Change controller mode back to Remo	te Run? Help
<	Ghannel 0, DF1     O1, 1769-L23E-Q8FC1 Ethernet Port     ✓	
Path: AB_I Path in Project: <nor< td=""><td>THIP-1\131.183.18.223\Backplane\0 e&gt;</td><td>Set Project Path</td></nor<>	THIP-1\131.183.18.223\Backplane\0 e>	Set Project Path

Fig. 4-49c Downloading to the CompactLogix Processor





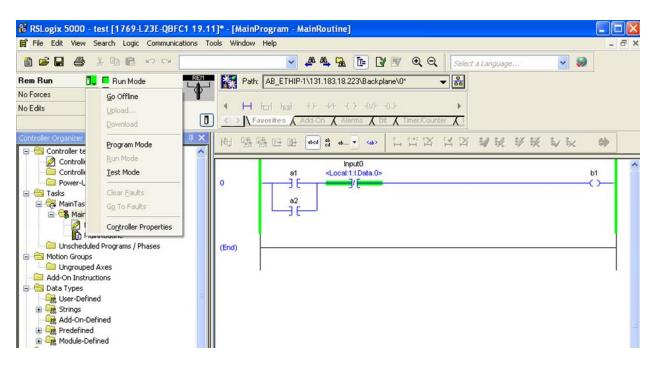


Fig. 4-51 Changing from Run to Program or Test Mode

👸 RSLogix 5000 - test [1769-L23E-QBFC1 19.11]* - [Program Tags - MainProgram]										
File Edit View Search Logic Communications To	ols Window Help									
🗋 📽 🖬 🎂 🕉 🍓 🗟 🕫 🗠 🗠	~		R. C. Select a Languag							
Rem Run . Run Mode	Path: AB_ETHI	P-1\131.183.18.223\Backplane\0*	▼ 💑							
No Edits		+ F + + + + + + + + + + + + + + + + + +	her/Counter							
Controller Organizer + 1 ×	Scope:	am 🔽 Show: All Tage	V 1							
Controller Tags	Name := 4	Value   Force Mask	<ul> <li>Style</li> </ul>							
- Controller Fault Handler	a1	0	Decimal							
Power-Up Handler	a2	0	Decimal							
B-Cal Tasks B-Cal MainTask	Input0	0	Decimal							
🖶 😋 Paniazo.	b1	0	Decimal							
Program Tags     MainRoutine     MainRoutine     Unscheduled Programs / Phases     Groups     Motion Groups     Motion Groups     Add-On Instructions										
Can Types     Can Types										
Add-On-Defined     Get Predefined     Get Module-Defined     Get Module-Defined     Get Tends										
Configuration     E CompactLogicS323E-Q8FC1 System     D 1769-L23E-Q8FC1 test										
T769-L23E-QBFC1 Ethernet Port LocalE      Setternet      CompactBus Local      Setternet      Debedded I/O										
[1] Embedded IQ16F Discrete     [2] Embedded OB16 Discrete _C     [3] Embedded IP4%OF2 Analog										
< >>	✓ Monitor Tags /	Edit Tags	×							
	(monitor rugs)	(2001-090)   C								

Fig. 4-52 Monitoring the Variables using Monitor Tags

## Exercises

- 1. \*In the program of Fig. 4-53, if input Catsup were changed from a NO contact to a NC contact, how would the program have changed to not change the function of the push button?
- 2. Would the program of Fig. 4-53 be significantly changed if the two N.C. contacts in the third rung were removed?
- 3. Where in the program of Fig. 4-53 could the copy/paste function have been used effectively?
- 4. Name the different processor modes for the Siemens S7-1200, the A-B L23E.

## Lab 4.1 The Hot Dog Counter

Project Description:

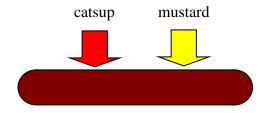
Fred and Rudy are making hot dogs at the ballpark. Fred dispenses mustard and Rudy dispenses catsup. A hot dog is not sold without each Fred and Rudy putting both mustard and catsup on the dog. As each pushes the button for their ingredient, a signal is fed to the PLC for the action. Either button may be pushed first. Design a program to count the total number of hot dogs made. Inputs should be wired to contacts and labeled as mustard and catsup. A display is kept in the PLC showing up-to-date counts of hot dogs made by Fred and Rudy.

To complete the lab, enter the program shown later in the lab into the PLC and wire the two inputs.

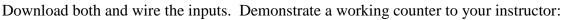
Watch the count accumulate in the counter as the two buttons are pressed in any order. Get a listing from the listing software on the programming software package. The documented listing of the program may be used as the final lab report.

Wire the PLC to the inputs for this lab and to inputs or outputs for other labs per the diagram on the next page.

The next page shows the layout of the PLC on the trainer and the PLC wiring schematic. To wire the two inputs, wire through the two pushbuttons selected so that 24 volts is at the terminals of I/0 and I/1 when the two buttons are pushed.



Enter the following 4 rung program in both Siemens TIA Portal and A-B RSLogix 5000.



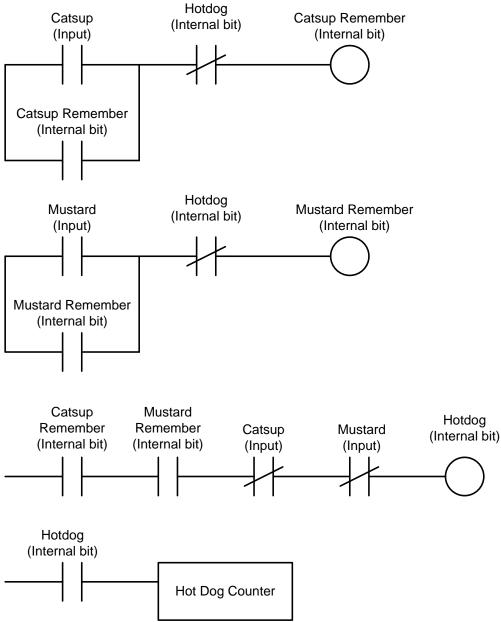
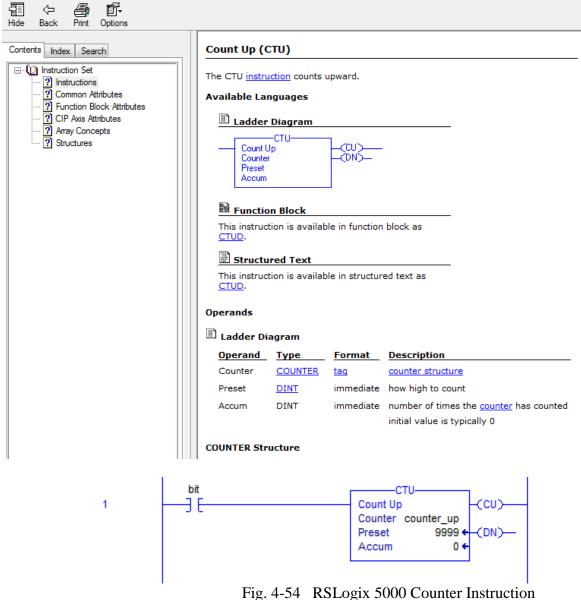
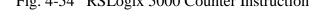


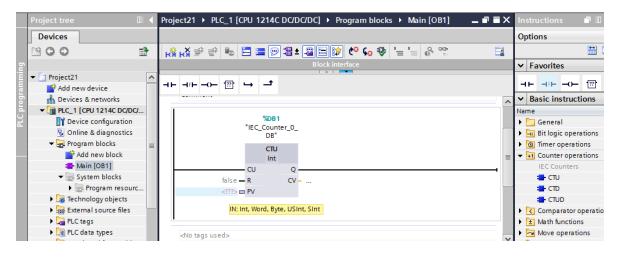
Fig. 4-53 Program to be Entered

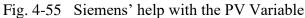
The count of hot dogs made is found in the accumulated value of the counter.

Both PLC platforms have instruction help features which may be used at this point to find how the counter function above is programmed. RSLogix 5000 has this feature in its Help>Instruction Help tab. Siemens has similar help features but has helps with the instruction to identify variable types in the instruction itself.









Ch. 4 Programming the Application

Project tree		Project21 → PLC_1 [CPU 1214C DC/DC/DC] → Program blocks → Main [OB1] 💦 🗕 🖬 🗮 🗙	Instructions 🛛 🗖 🔳 🕨
Devices			Options
🖻 O O	<b>a</b>	📸 📸 学 🐏 🚍 🚍 💬 📲 ± 🖀 😫 😢 🚱 🍄 '= '= 🔗 🐃 🛛 📑	H 🗆
		Block interface	✓ Favorites
<ul> <li>Project21</li> </ul>	^		
💕 Add new device			
Devices & networks			Basic instructions
PLC_1 [CPU 1214C DC/DC/.			Name
Device configuration		%DB1	🕨 🔄 General 🔷
🧏 Online & diagnostics		"IEC_Counter_0_ DB"	🕨 🔄 Bit logic operations 📃
🕶 📴 Program blocks	=	СТ	G Timer operations
📑 Add new block			
💶 Main [OB1]		CU 0	IEC Counters
🗢 🔄 System blocks		false – R CV – L.	🚍 СТО
Program resourc.		?? — PV	🚍 СТО
🕨 🙀 Technology objects		OUT: Int, Word, Dint, Real, LReal	🖶 CTUD
🕨 🔚 External source files			▶ 🔇 Comparator operatio
🕨 🔁 PLC tags			• 主 Math functions

Fig. 4-56 Siemens' help with the CV Variable

In the example above, Figs. 4-51 and 4-52 show the type of inputs available for PV and CV. In general, PV is short for process variable and CV is short for the controlled variable. For the upcounter, PV is the count preset and CV is the active count. The PV may hold a constant as shown below:

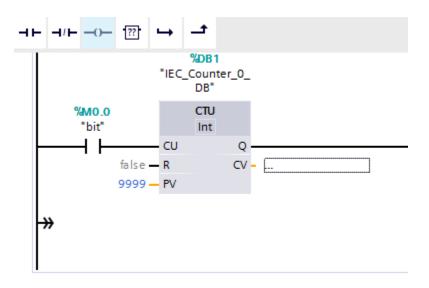


Fig. 4-57 Siemens' CTU Instruction with PV Constant (=9999)

This work is licensed under a Creative Commons Attribution 4.0 International License.